

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicants:</b>	<b>Koichi Ohto, et al.</b>	<b>Examiner:</b>	<b>Unassigned</b>
<b>Serial No:</b>	<b>Unassigned</b>	<b>Art Unit:</b>	<b>Unassigned</b>
<b>Filed:</b>	<b>Herewith</b>	<b>Docket:</b>	<b>17408</b>
<b>For: SEMICONDUCTOR DEVICE, AND PRODUCTION METHOD FOR MANUFACTURING SUCH SEMICONDUCTOR DEVICE</b>		<b>Dated:</b>	<b>January 29, 2004</b>


Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**CLAIM OF PRIORITY**

Sir:

Applicants in the above-identified application hereby claim the right of priority in connection with Title 35 U.S.C. § 119 and in support thereof, will submit in due course a certified copy of Japanese Patent Application No. 2004-018079, filed on January 27, 2004 & Japanese Patent Application No. 2003-021076, filed on January 29, 2003.

Respectfully submitted,

  
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
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Dated: January 29, 2004

  
Paul J. Esatto, Jr.